CLIPPING DEVICE

ABSTRACT OF THE DISCLOSURE

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A clipping device able to reduce the number of processing cycles, able to simplify the circuit, and able to increase the speed of the processing, having a clip code generation circuit for generating clip codes corresponding to results of comparison of vertex coordinates of a primitive and a judgment reference value of a multi-dimensional region and a negative value of the judgment reference value for the vertexes of the primitive; a current clip register for shifting the clip codes; clip registers able to replace the clip codes in accordance with a control signal; a control circuit outputting a control signal so as to replace the clip codes between clip registers when receiving a replacement instruction; and a logic circuit for performing a logic operation with respect to all bit data set in the clip registers and setting a clip flag indicating whether or not the vertex to be judged is inside or outside a multidimensional region of an object to be drawn.

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